Rover RCRF—ReConfigurable RF

Fully ReConfigurable RF Board with Design, Simulation & Synthesis Software Using NI AWRs VSS & Microwave Office

PRODUCT DESCRIPTION

Rover RCRF is CEI’s latest reconfigurable RF board with design, simulation, and synthesis software using NI AWRs VSS and Microwave Office. Quickly create an Up Converter, Down Converter, Heterodyne amplifier, or a variety of RF systems using this reconfigurable RF system demonstrator. In minutes, you can design a circuit, simulate it, realize it in hardware, and see how it performs on your lab bench. Then quickly redesign however you’d like: try a different filter, add an amplifier block or notch out a spur, and see the results instantly. Choose the Rover RCRF for your next RF design!

APPLICATIONS

- Wireless communication systems
- Test & measurement
- Automated Test Equipment (ATE)
- Proprietary wired/wireless communication systems
- Frequency planning
- Radar systems
- EW, ECM, ESM and other RF and microwave systems

PRODUCT DESCRIPTION

- Design, simulate & test in minutes in NI AWRs VSS window
- Test out unlimited designs with no board re-spins
- Almost 20,000 configurations
- Drag and drop user interface using NI AWRs VSS window
- ‘How To’ CD included
- Development of component specifications
- Algorithm development
- Creation of modulated signals
- Co-simulation with circuit tools
- End-to-end simulations
- Predicting wireless conformance tests
- Hardware-in-the-loop simulations

HIGHLIGHTS

- Reconfigurable RF system demonstrator lets you specify your RF circuit, simulate it, then instantly configure it in hardware.
- Connect to a signal generator and spectrum analyzer, then change your design and see the RF effects in real time.
- Design is simple—drag desired components from the component library and drop them onto the screen.
- Design check software (included) lets you know whether the device can create the system you’ve drawn.
- Design, simulate, synthesize and configure the device without ever leaving AWRs VSS window.
- Save your designs to share or work with later.
- Hardware device can be programmed and reprogrammed as often as you like.
- Device configuration is non-volatile even when removed from the design workstation. The device maintains your design when powered back on.
- Design-to-device fidelity is ensured by internal modeling, error detection and positive acknowledgement architecture.

BENEFITS

- Design, simulate & test in minutes in NI AWRs VSS window
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FEATURES

- Hardware module implements 39 selectable RF components in five stages, including one pass-through for each stage.
- Stage five contains a band reject filter with software tunable center frequency.
- Six SMA connections
- RF input and output
- Connections out to an external circuit and back in
- LO input to Sum mixer
- LO input to diff mixer
- Uses AWR’s VSS software for data input and simulation
- Includes component library, all DRC and synthesis software and controller module.
- Non-volatile memory restores the last configuration on power-up or reset
- Includes users guide and 6’ USB cable to connect the designers PC to the RF-FPGA.
- Most onboard components operate around 70 MHz, 811.5 MHz, or 1960 MHz.

CONFIGURATIONS

<table>
<thead>
<tr>
<th>Model Number</th>
<th>Configuration</th>
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<tbody>
<tr>
<td>RCRF-SW</td>
<td>RCRF board with first year software support with five support hours included — VSS &amp; microwave office required — not included</td>
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<tr>
<td>RCRF-CUSTOM</td>
<td>Affordable customization for your application space</td>
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DESIGN FLOW

- Design In AWR VSS
- Simulate in AWR VSS
- CEI Design Rule Checker & Synthesizer
- Download to RCRF
- Test on RCRF

- Fixed or adaptable reconfigurable RF architecture
- Reconfigurable frequency agile up and down converters
- Reconfigurable C, S and L band Radar transceiver
- Reconfigurable RF, IF and Baseband filter arrays
- Programming and RF verification and simulation (Microwave office/VSS)
- Single platform design = 19,683 different RF topologies
- Demonstration of RF Theory in classroom setting
- Transceiver Frequency Planning in laboratory setting